DECLARATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or the below named inventors are the original, first and joint inventors (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled SEMICONDUCTOR DEVICE AND METHOD FOR LOWERING MILLER CAPACITANCE FOR HIGH-SPEED MICROPROCESSORS, the Specification of which:

	\boxtimes	is attached hereto. was filed on	as Applicatio	on Serial No	·	
including	•		viewed and unde	erstand the con	ntents of the ab-	ove-identified specification,
material	to pater	wledge the duty to disc stability of the subject Regulations, § 1.56.	close to the Paten matter claimed	t and Tradema in this applica	rk Office all info tion, as "materia	ormation known to me to be ality" is defined in Title 37,
patent, U any forei	nited St gn appli	ates provisional applic	ation(s), or inven ed States provisio	tor's certificate nal application	listed below and	ny foreign application(s) for d have also identified below extificate having a filing date
		PRIO	RITY APPLICA	TION(S)		Priority Claimed
(Numb	er)	(Countr	y)	(Date File	d)	Yes/No
(Numb	er)	(Countr	y)	(Date File	d)	Yes/No
listed bel United S acknowle claimed	low and states ap edge the in this available	, insofar as the subject plication in the manner duty to disclose all in application, as "materi	matter of each or or provided by the aformation known ality" is defined	f the claims of e first paragraj to me to be n in Title 37, C	this application ph of Title 35, Unaterial to patent ode of Federal	United States application(s) is not disclosed in the prior United States Code, § 112, I tability of the subject matter Regulations, § 1.56, which T international filing date of
-	(Applic	eation Serial No.)	(Filing Date)		(Status)	
-	(Applic	ation Serial No.)	(Filing Date)		(Status)	

I hereby direct that all correspondence and telephone calls be addressed to Randall C. Furlong, Ph.D., Williams, Morgan & Amerson, P.C., 7676 Hillmont, Suite 250, Houston, Texas 77040, (713) 934-4061.

(Application Serial No.)

I HEREBY DECLARE THAT ALL STATEMENTS MADE OF MY OWN KNOWLEDGE ARE TRUE AND THAT ALL STATEMENTS MADE ON INFORMATION AND BELIEF ARE BELIEVED TO BE TRUE; AND FURTHER THAT THESE STATEMENTS WERE MADE WITH THE KNOWLEDGE THAT WILLFUL FALSE STATEMENTS AND THE LIKE SO MADE ARE PUNISHABLE BY FINE OR IMPRISONMENT, OR BOTH, UNDER SECTION 1001 OF TITLE 18 OF THE UNITED STATES CODE AND THAT SUCH WILLFUL FALSE STATEMENTS MAY JEOPARDIZE THE VALIDITY OF THE APPLICATION OR ANY PATENT ISSUED THEREON.

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Inventor's Full Name:	MICHAEL P. DUANE					
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In re Application of:

§

§

§ Examiner: Unknown

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§ Group Art Unit: Unknown §

Serial No.: Unknown

Atty Docket: 2000.032100/TT3633

Concurrently Herewith Filed:

For:

SEMICONDUCTOR DEVICE AND METHOD FOR

LOWERING MILLER CAPACITANCE FOR

HIGH-SPEED MICROPROCESSORS

§ § § §

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POWER OF ATTORNEY

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

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I.F

The undersigned, being the inventors named in the above-identified application, hereby revoke any previous Powers of Attorney and appoint:

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as its attorney or agent so long as they remain with such firms, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

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